

ECE 440L

Fall 2013

Last updated: 09/18/13

## Lab 4 rubric

### Overview

- 100 points possible
- Grade basis: Completeness, clarity, and correctness
- Assign partial credit when appropriate.

Problem	Item
1 (b)	Circuit is recorded.
	Result is recorded.
2 (a)	Properly identify DSBSC versus DSBLC
(b)	Properly identify amplitude and frequency of each message
3 (b)	Stolen carrier demodulation done
(c)	Effect of phase difference <ul style="list-style-type: none"><li>- Empirical</li><li>- Theoretical</li></ul>
(d)	Effect of frequency difference <ul style="list-style-type: none"><li>- Empirical</li><li>- Theoretical</li></ul>
4 (x)	Block diagram of Costas loop
	... with signals labeled
(y)	How does Costas loop determine phase error?
(z)	How does VCO implement negative phase feedback?